U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Attorney Docket No.: P2000,0342 Appl. No.: Applicant: FLORIAN SCHAMBERGER					
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N	Α	5,844,298	12/1/98	Smith et al.	257	330		AIE .	
N	В	5,821,800	10/13/98	Le et al.	327	333	 		
n	С	5,696,728	12/9/97	Yu et al.	365	218	1	· · ·	
M	D	5,448,187	9/5/95	Kowalski	326	81	_		
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N	J	693 01 225 T2	5/25/94	Germany			120		
n	K	0 798 860 A2	10/1/97	Europe				•	
γ	L	0 388 074 A1	9/19/90	Europe	-				
N	М	62081185	4/14/87	Japan					
M	N	60018018	1/30/85	Japan	-				
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w	М	04145720	5/19/92	Japan					
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Sheet 1 of 1 FORM PTON 449 (SUBSTITUTE) Attorney Dock t No.: Applic, No. P2000.0342 10/600,916 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Applicant INFORMATION DISCLOSURE Florian Schamberger STATEMENT BY APPLICANT Filing Date (37 CFR 1.98(b)) **Group Art Unit** June 20, 2003 U.S. PATENT DOCUMENTS **EXAMINER** SUB FILING INITIALS. PATENT NO. DATE PATENTEE **CLASS CLASS** DATE Α 5,293,561 03/08/94 Nizaka 36*5* 189_11 В 5,313,424 05/17/94 Adams et al. 200 C 6,222,384 B1 04/24/01 Kim 68 326 D Ε F G Н FOREIGN PATENT DOCUMENT SUB TRANSL. DOCUMENT NO. DATE COUNTRY **CLASS** YES | NO CLASS J 198 25 034 A1 04/15/99 Germany X Κ 98/35444 08/13/98 **WIPO** X · М Ν OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) ō Joo-Sun Choi et al.: "Antifuse EPROM Circuit for Field Programmable DRAM",

2000 IEEE International Solid-State Circuits Conference, pp. 406-407

Jae-Kyung Wee et al.: "An Antifuse EPROM Circuitry Scheme for Field-Programmable Repair in DRAM", IEEE Journal of Solid-State Circuits, Vol. 35, No. 10, October 2000, pp. 1408-1414

EXAMINER DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with n xt communication to applicant.